Schematic Package Supplement to

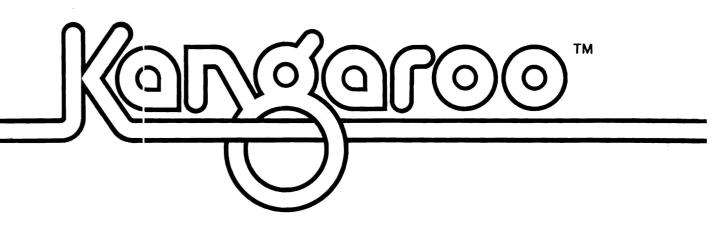
Table of Contents

Sheet 1A You Are Here

Sheet 1B	Upright Kangaroo Game Wiring Diagram (038577-01 A)							
Sheet 2A	reland-Built Kangaroo Game Wiring Diagram (038451-01 A)							
Sheet 2B	Color Raster-Scan Power Supply Wiring Diagram (037669-01 C)							
Sheet 3A	Regulator/Audio II PCB Schematic Diagram (035435-06 B)							
Sheet 3B	Coin Door Wiring Diagram (037542-01 B), Utility Panel Wiring Diagram (038004-01 Fluorescent Light and Speaker Wiring Diagram (035833-01 A)							
C	entral Processing Unit (CPU) PCB Schematics (038469-01 A), Sheets 4A-7A							
Sheet 4A	Block Diagram							
Sheet 4B	Memory Map, CPU Power Input							
Sheet 5A	Game Microprocessor, Address Decoder							
Sheet 5B	Program ROM, RAM							
Sheet 6A	Inputs, Outputs							
Sheet 6B	Sound Microprocessor, Address Decoder							
Sheet 7A	Custom Microcomputer							
	Video PCB Schematics (038468-01 A), Sheets 7B-11B							
∙Sheet 7B	Block Diagram							
Sheet 8A	Dynamic RAM Timing Diagram, Video Power Input							
Sheet 8B	Clock, Sync Chain and Timing Signals, Dynamic RAM Control, Address Decoding							
Sheet 9A	DMA Control, Dynamic RAM Video Address and Flip							
Sheet 9B	Picture ROM Address Selector, Picture ROM							
Sheet 10A	Dynamic RAM Address from CPU and for DMA							
Sheet 10B	Dynamic RAM Data Selector, Address Selector, and Control Latches							
Sheet 11A	Playfield (Dynamic RAM A)							

Sheet 12A Electrohome 19-Inch Color Raster-Scan Video Display Schematic Diagram

Sheet 11B Motion Object (Dynamic RAM B), Video Output



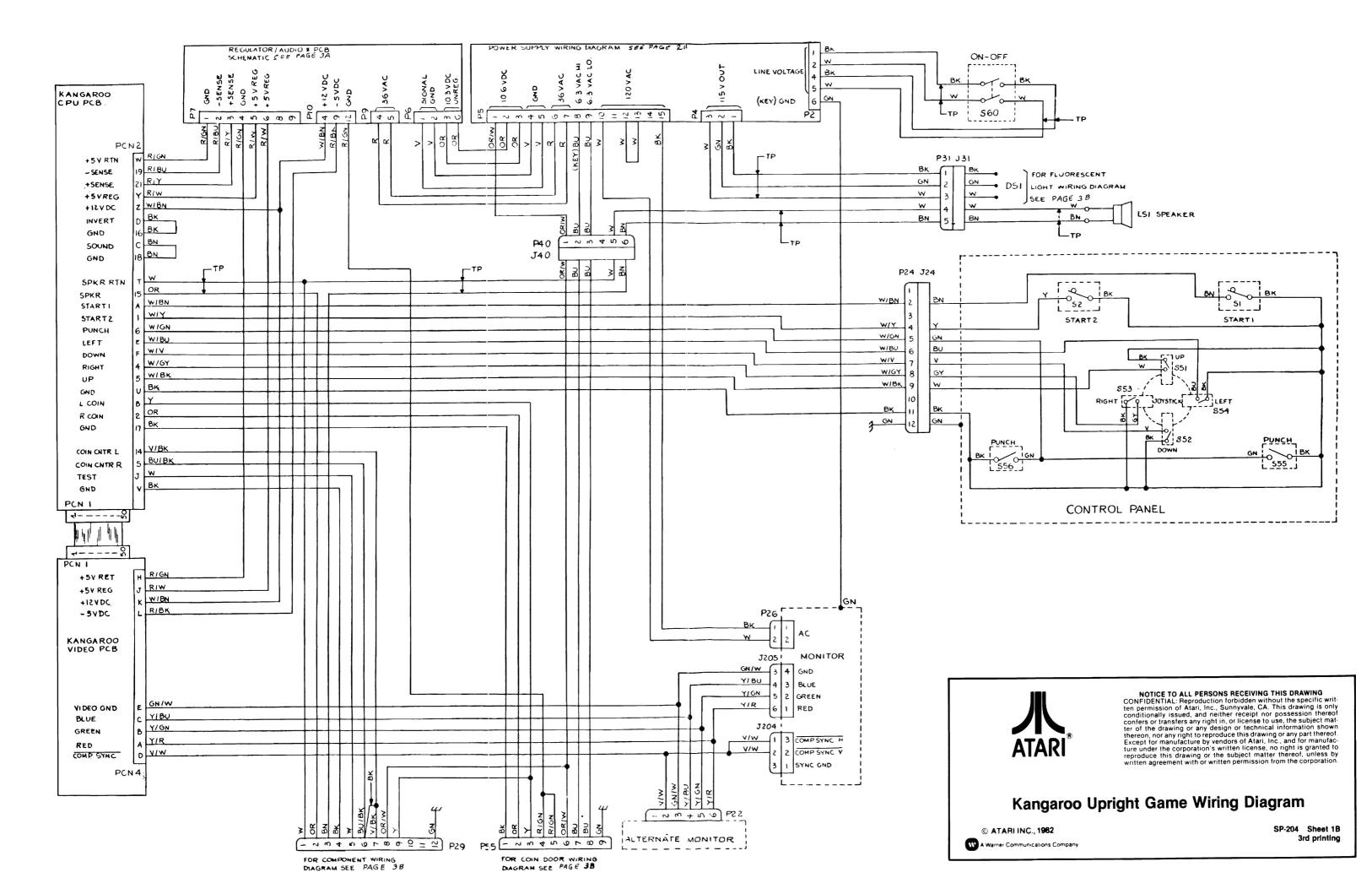
Operation, Maintenance and Service Manual

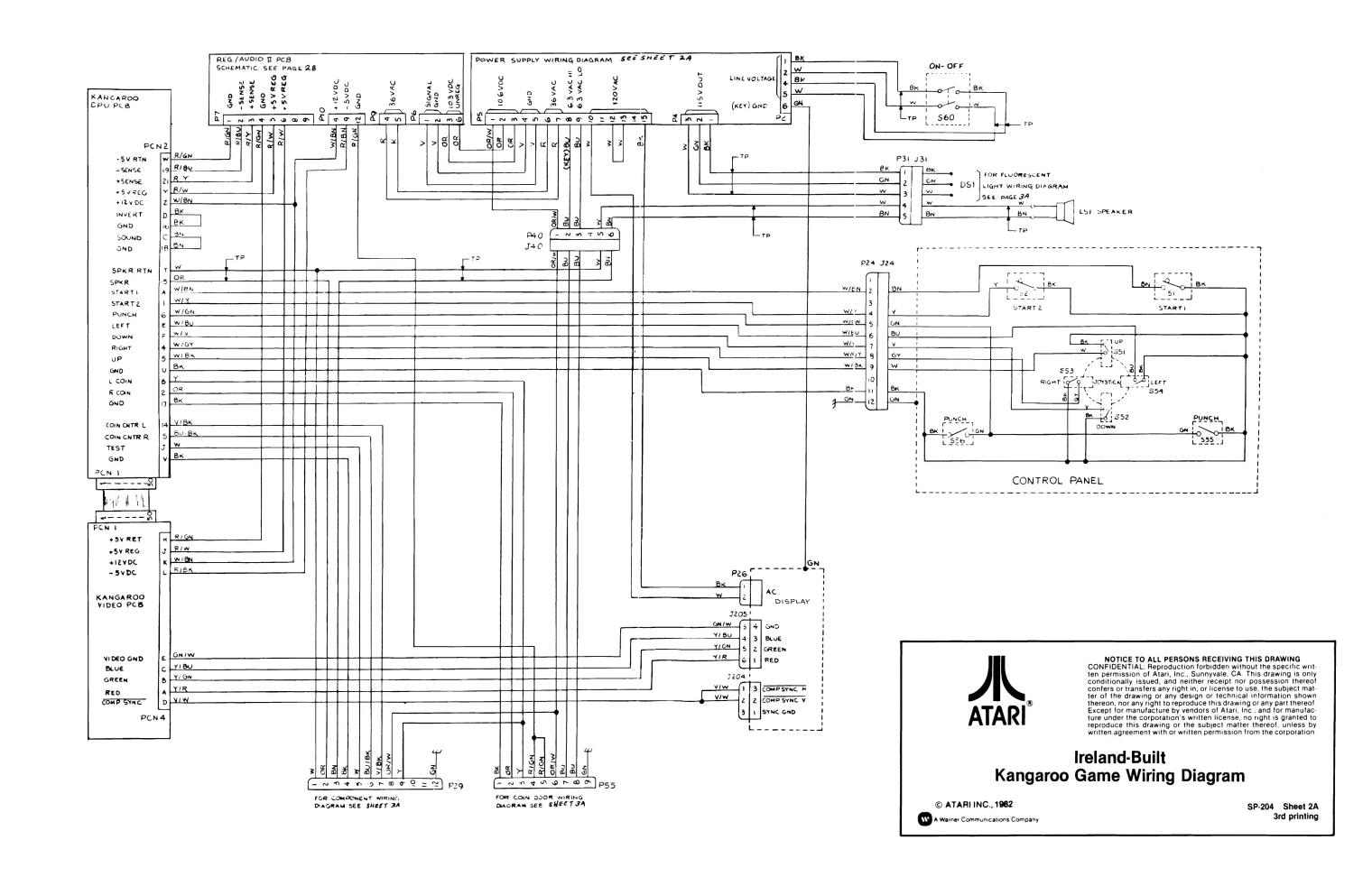
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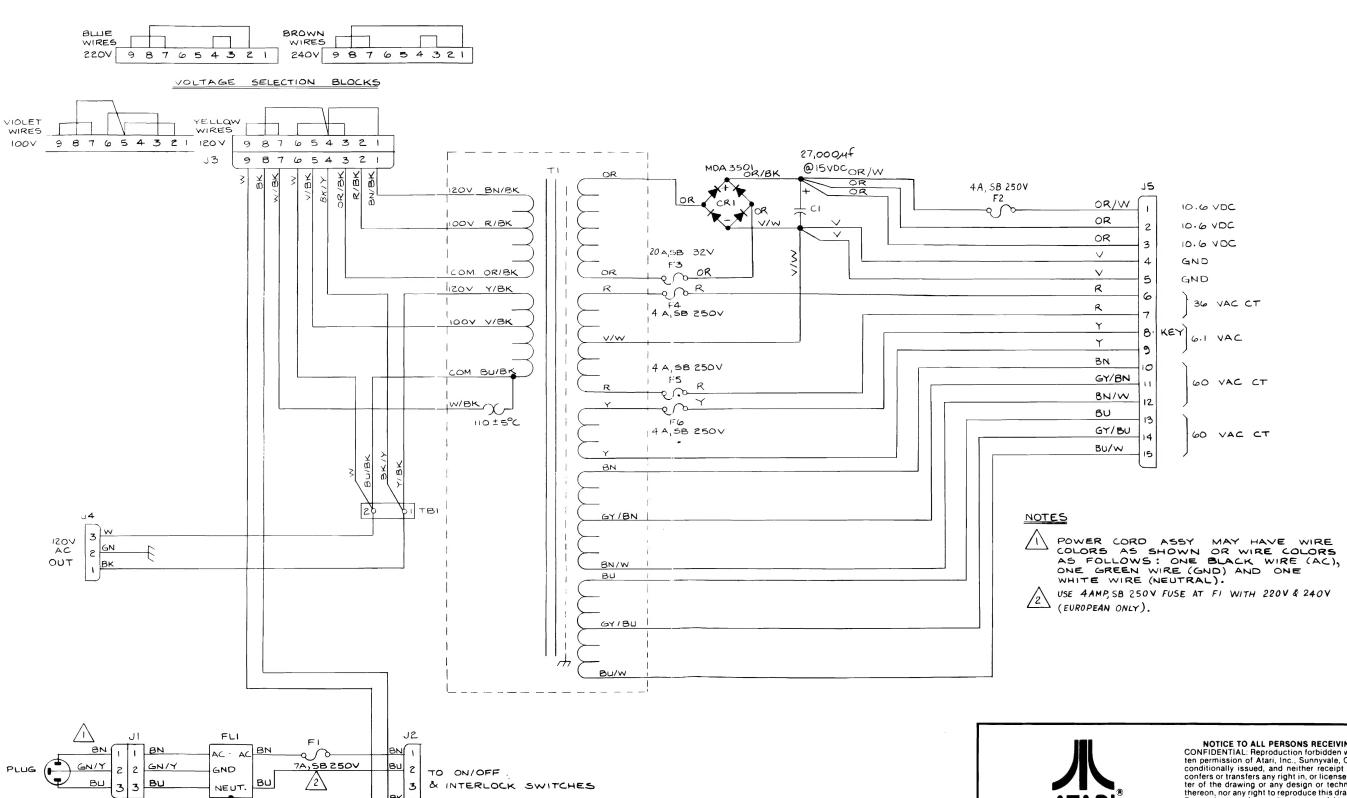
B),

This staple temporarily holds the schematic package together. Remove the staple before using the schematics.









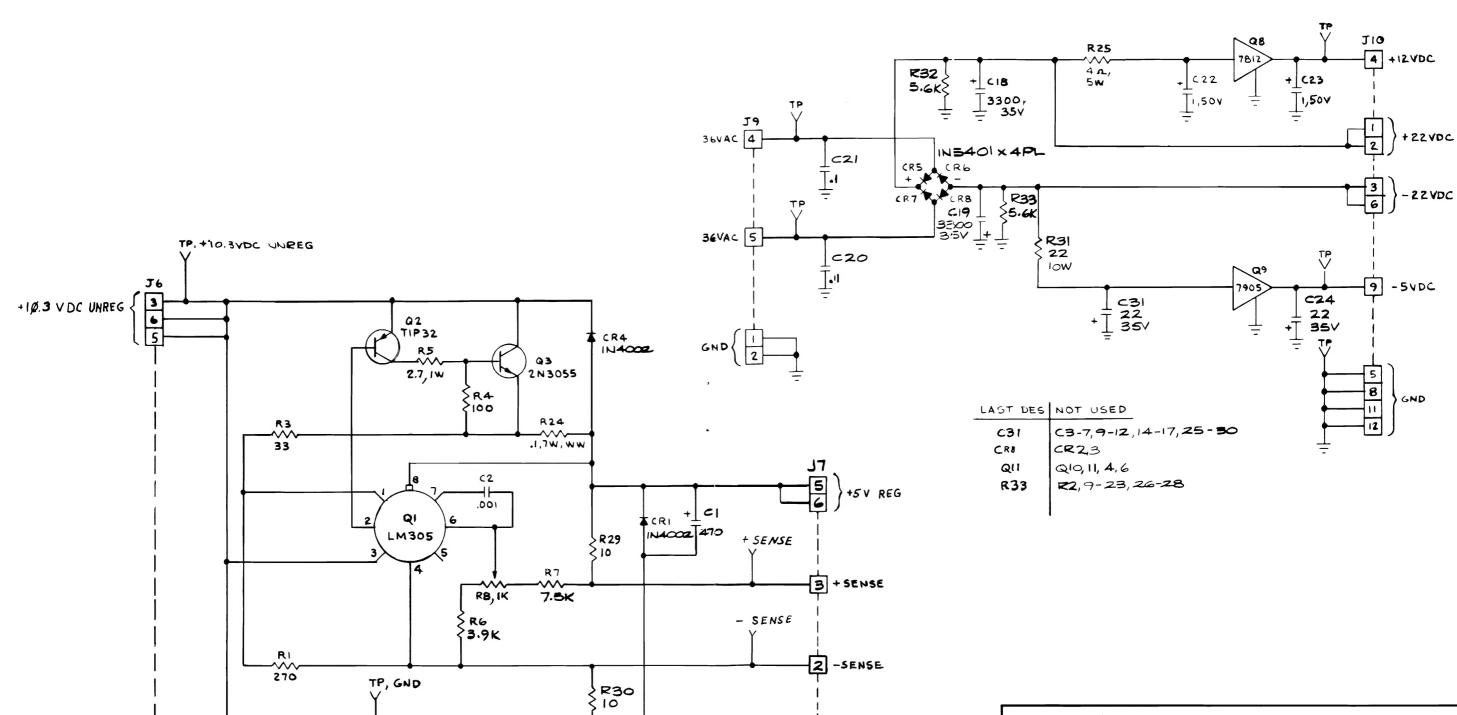


Kangaroo Power Supply

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SP-204 Sheet 2B



SIG. GRD

ce ⊥ .22 ⊤



+5V RTN

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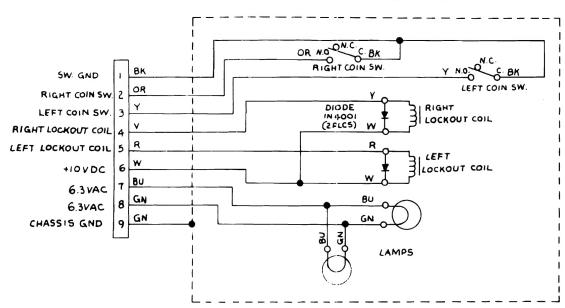
Kangaroo Reg./Audio II PCB Schematic Diagram

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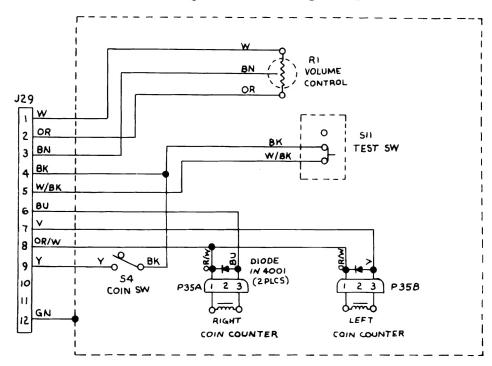
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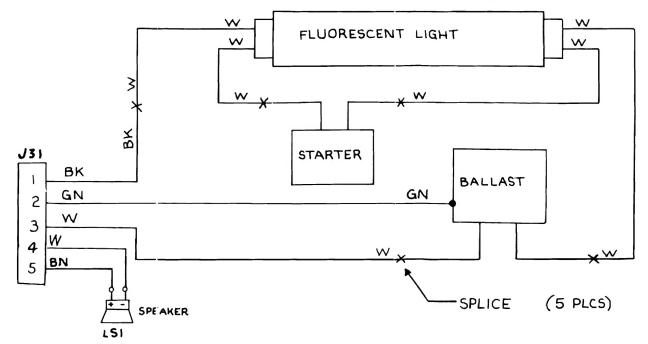
Coin Door Wiring Diagram



Utility Panel Wiring Diagram



Fluorescent Light and Speaker Wiring Diagram





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Kangaroo Game Wiring Interfaces

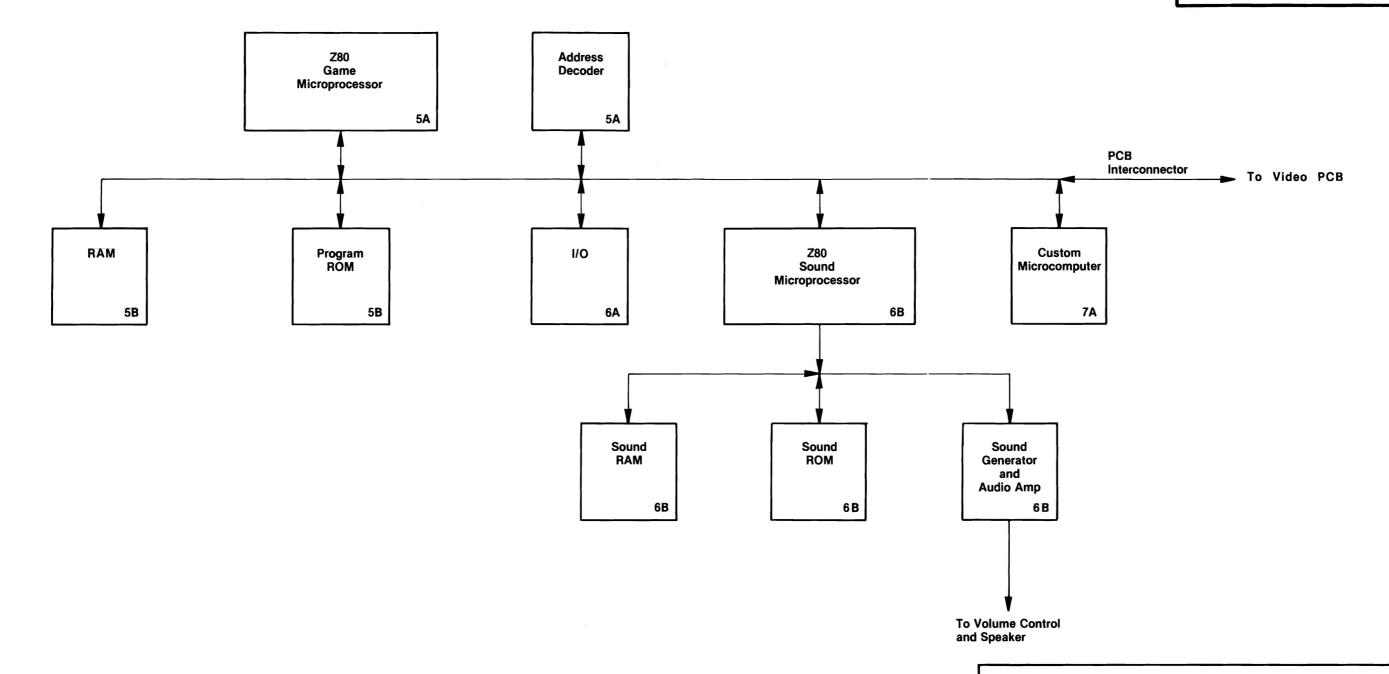
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Block Diagram (CPU PCB)

The service switch, located on the CPU PCB, allows you to enter credits without tripping the coin counter.





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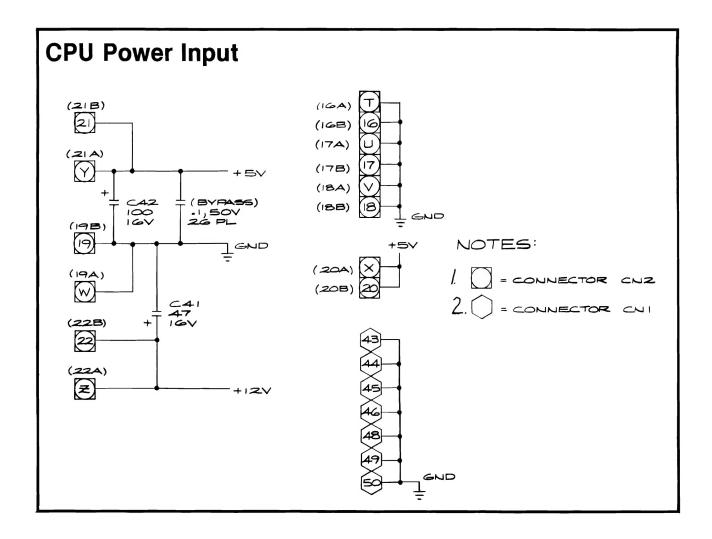
Kangaroo CPU PCB Schematic Diagram

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Memory Map

НЕХА-												
DECIMAL ADDRESS	R/W	D7	D6	D5		ATA D3	D2	D1	DO	FUNCTION		
ADDRESS D7 D6 D5 D4 D3 D2 D1 D0 Game Microprocessor Memory Space (IC15)												
0000-5FFF	R	D	D	D	D	D	D	D	D	Z80 24K Program ROM		
E000-E3FF	R/W	D	D	D	D	D	D	D	D	1K Working RAM		
E400	R	D	D	D	D	D	D	D	D	Option Switch		
E800 E801 E802 E803 E804 E805 E806 E807 E808 E809 E809	W W W W W W W		D	D D D D D D D D D	D D D D D D D D	D D D D D D D D	D D D D D D D D	0000000000	00000000000	Low Byte Start Address of Data in Picture High Byte Start Address in Bit Map RAM (where High Byte picture is to be written) During DMA Low Byte Picture Size for DMA High Byte and DMA Start Vertical Start Address in Bit Map Horizontal Start Address in Bit Map Bank Select Latch A & B Bit Map Control Latch (A = playfield, B = motion) Color-Shading Latch		
EC00 EC00 EC00 EC00 EC00 EC00	W R R R R	D	D	D	D D	D D	D D	D D	D	Sound DATA to Sound Microprocessor Utility Coin Switch 1 Player Start 2 Player Start Left Coin Input Right Coin Input		
ED00 ED00 ED00 ED00 ED00 ED00 ED00	W W R R R R				D	D	D	D D	D D	Coin Counter 1 Coin Counter 2 (European games) Player 1 Right Player 1 Left Player 1 Up Player 1 Down Player 1 Punch		
EE00 EE00 EE00 EE00 EE00	R R R R R				D	D	D	D	D	Player 2 Right Player 2 Left Player 2 Up Player 2 Down Player 2 Punch		
EFXX EFXX	W R					D D	D D	D D	D D	Output to Custom Microcomputer Input from Custom Microcomputer		
			S	ound	d Mi	icrop		ess	or M	lemory Space (IC34)		
0000-0FFF 4000-43FF	R R/W	D D	D D	D D	D D	D D	D D	D D	D D	4K Program ROM 1K Working RAM		
6000	R	D	D	D	D	D	D	D	D	Read DATA from Game Microprocessor		
7000 8000	W R	D D	D D	D D	D D	D D	D D	D D	D D	Write to Sound Chip (GI-AY-3-8910) Read from Sound Chip		

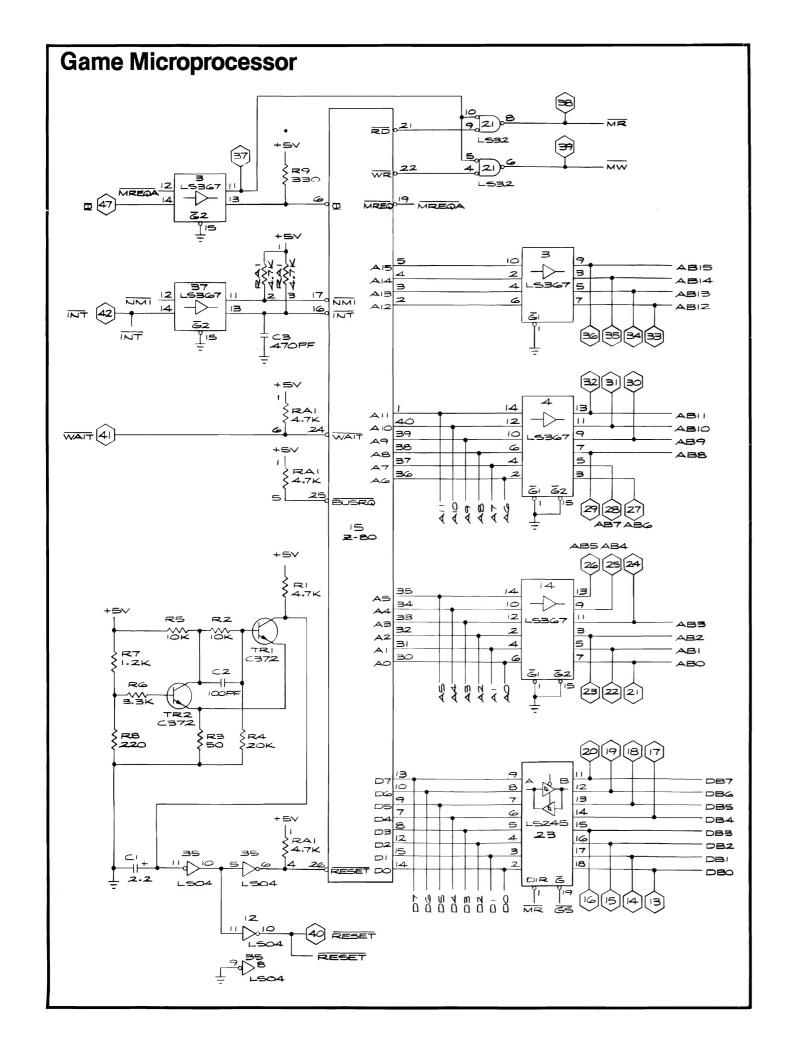


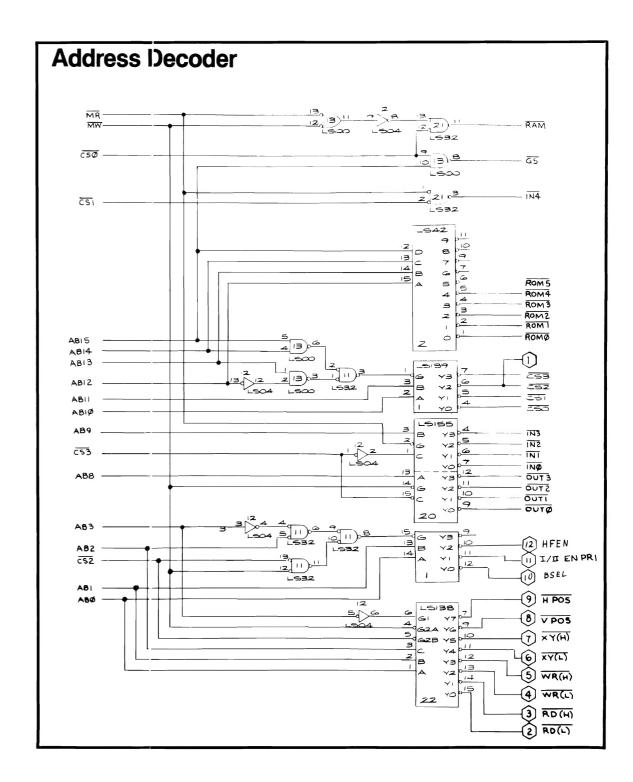


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Kangaroo CPU PCB Schematic Diagram

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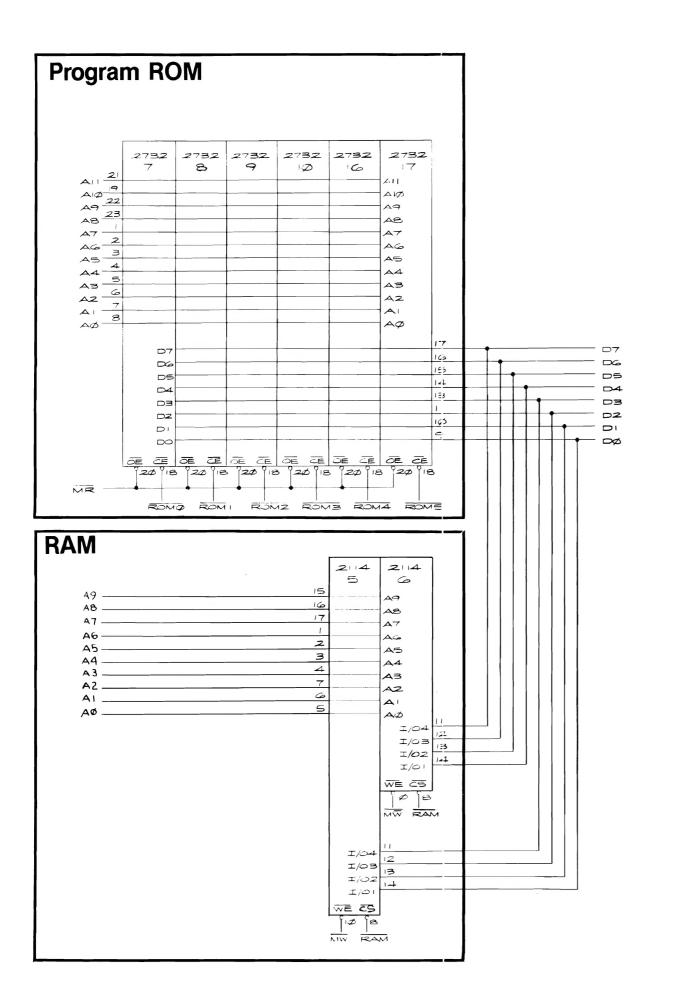


Kangaroo CPU PCB Schematic Diagram

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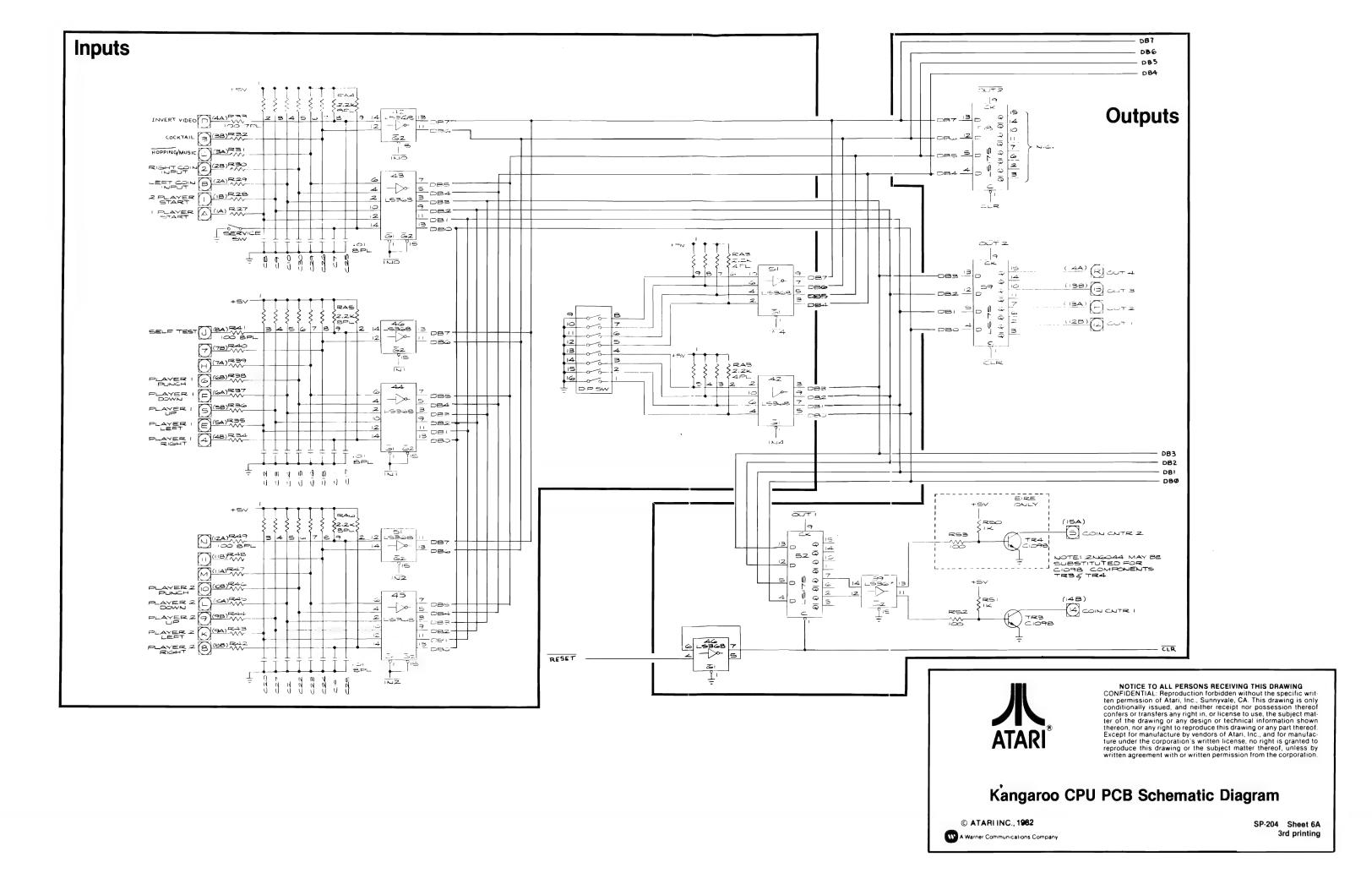


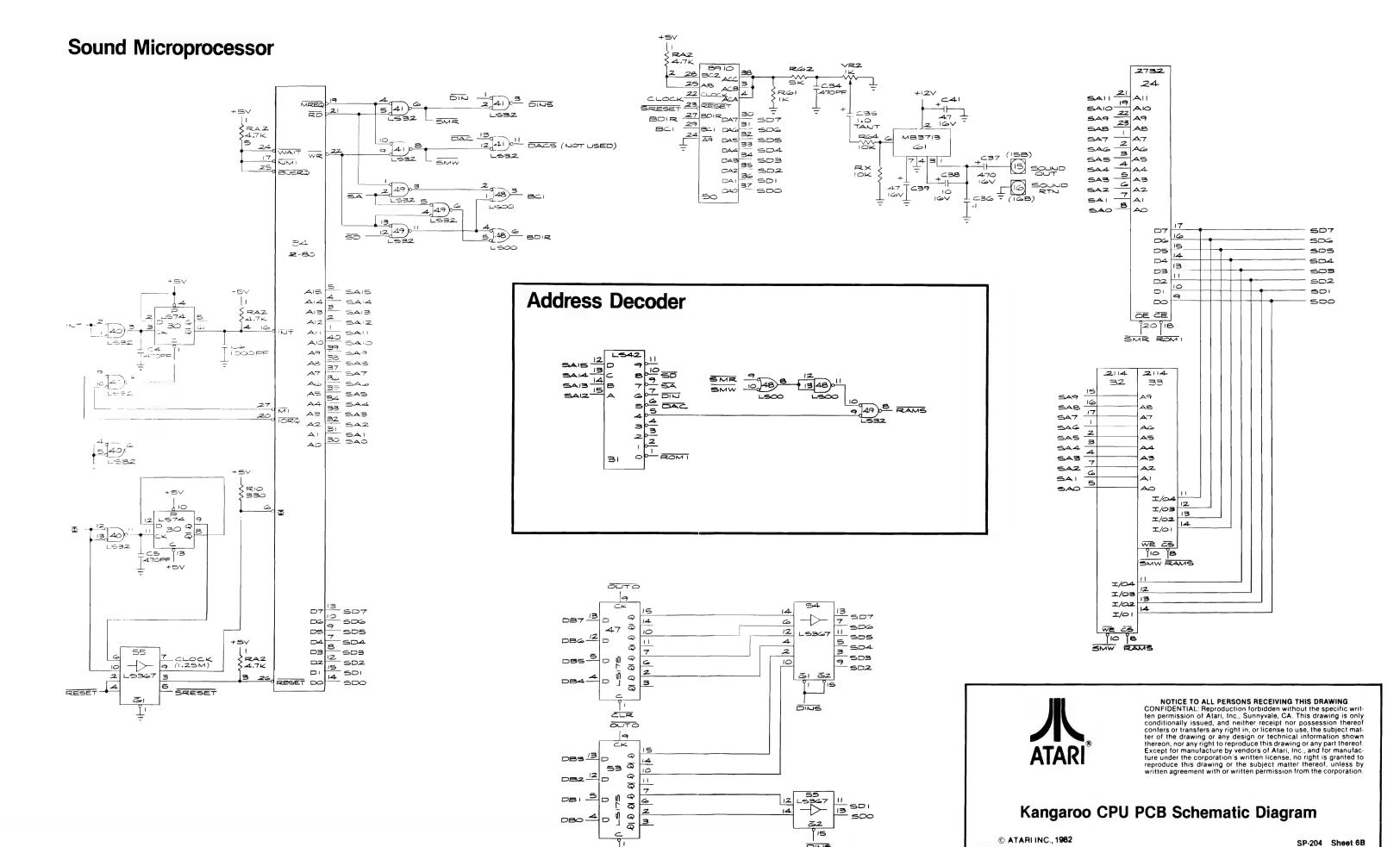
Kangaroo CPU PCB Schematic Diagram

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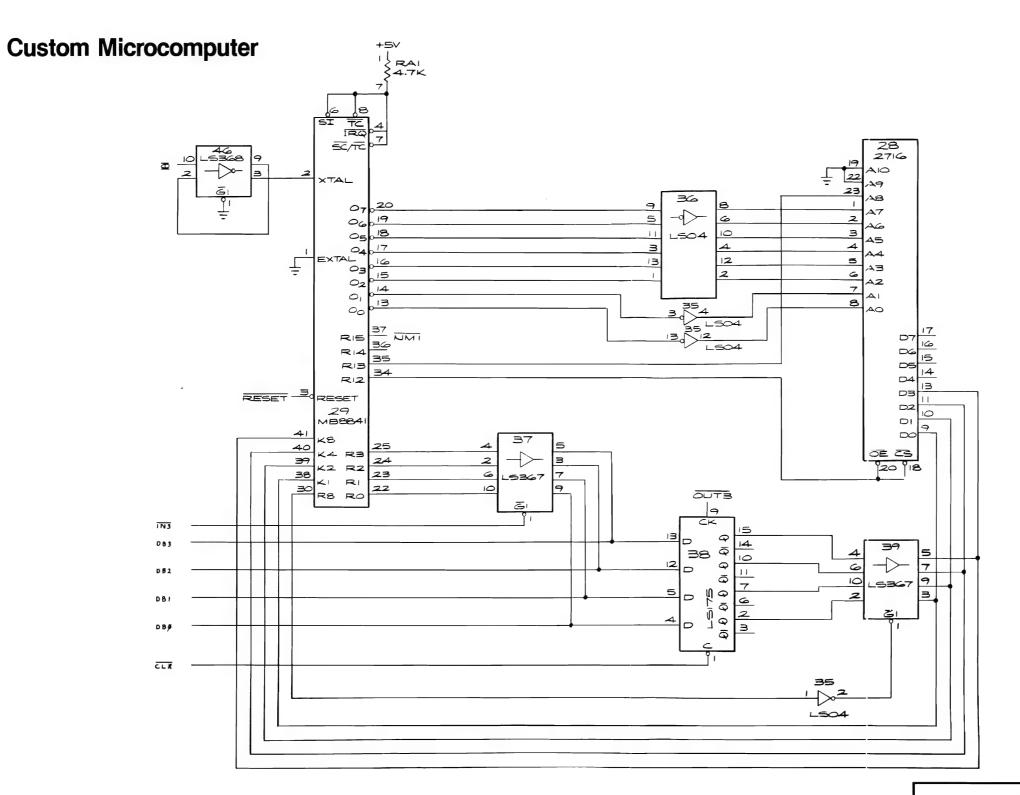
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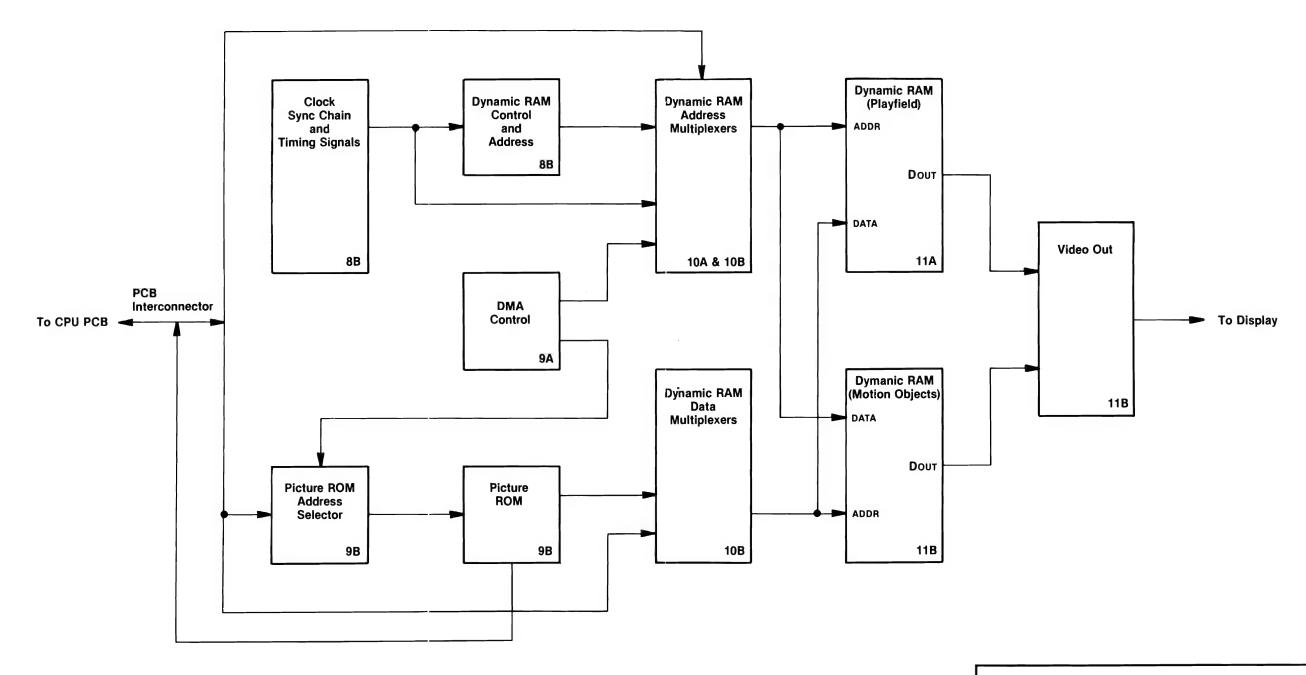


Kangaroo CPU PCB Schematic Diagram

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Block Diagram (Video PCB)





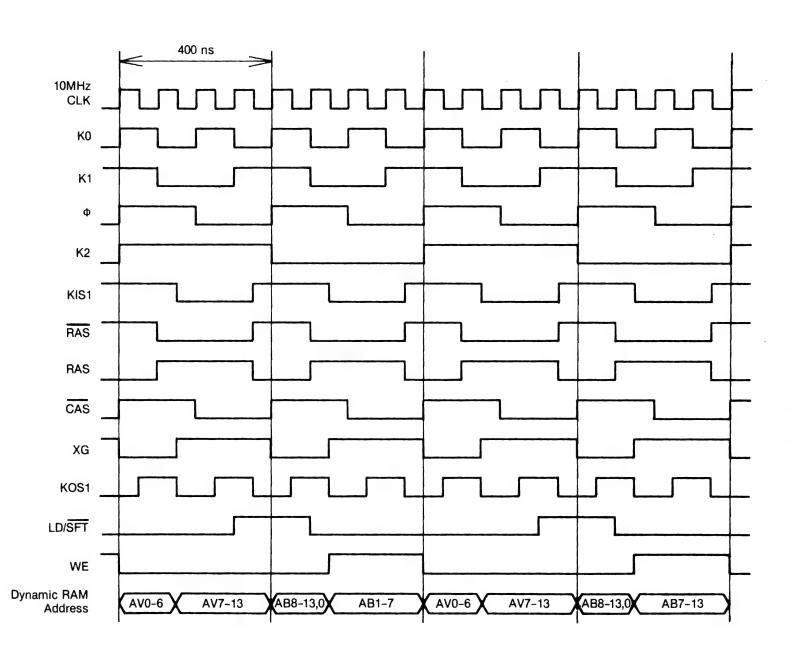
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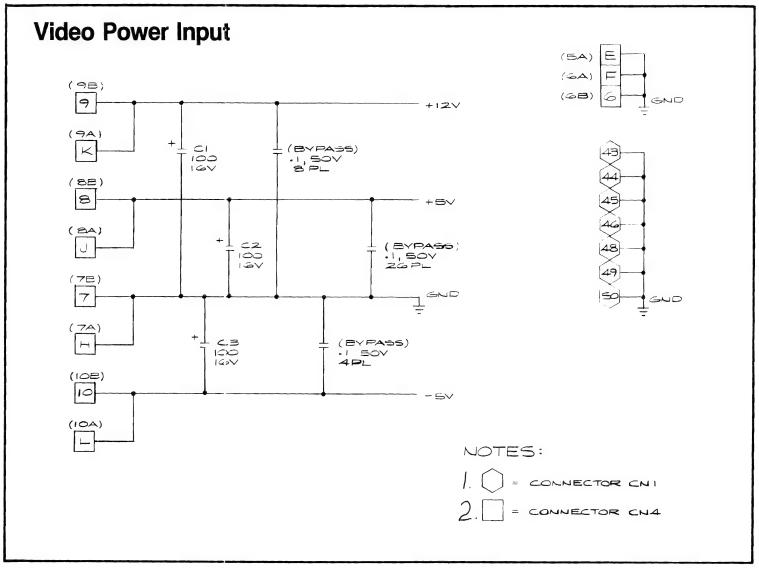
Kangaroo Video PCB Schematic Diagram

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Dynamic RAM Timing Diagram (Video PCB)







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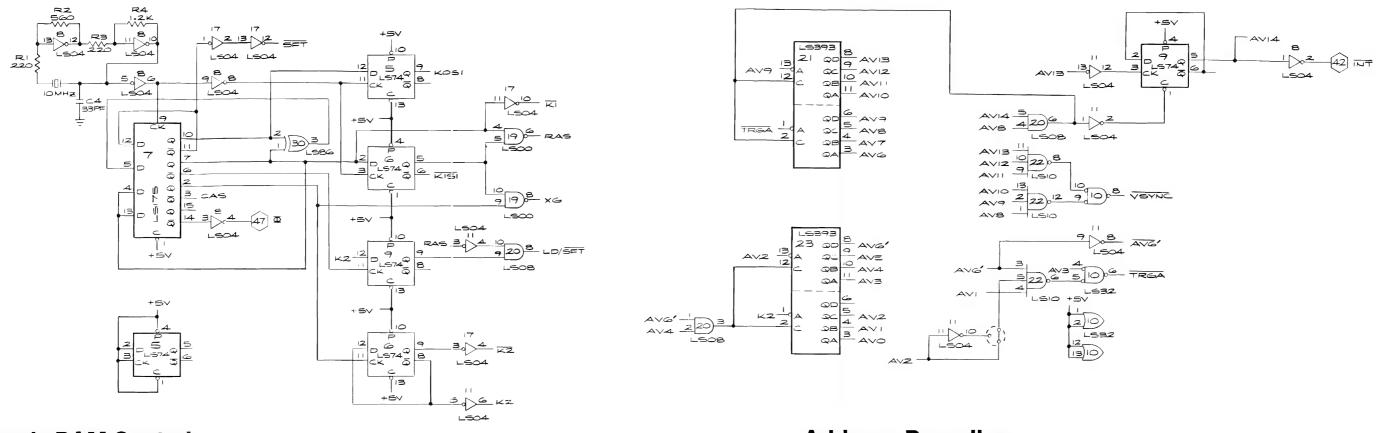
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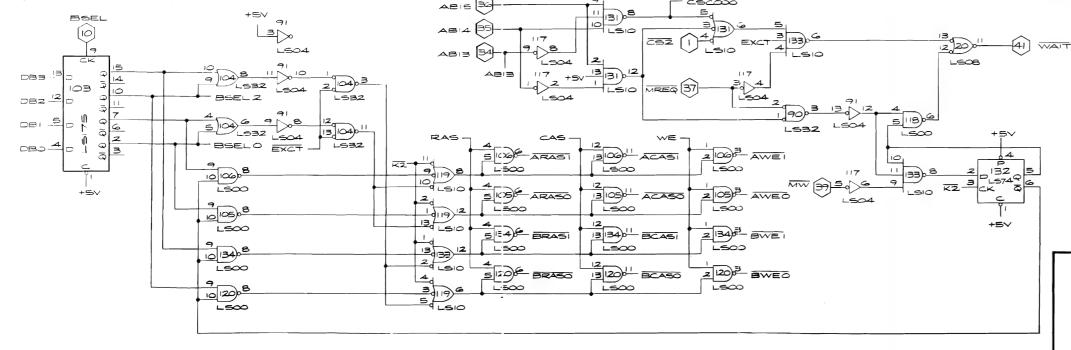
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Clock, Sync Chain, and Timing Signals



Dynamic RAM Control

Address Decoding





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Kangaroo Video PCB Schematic Diagram

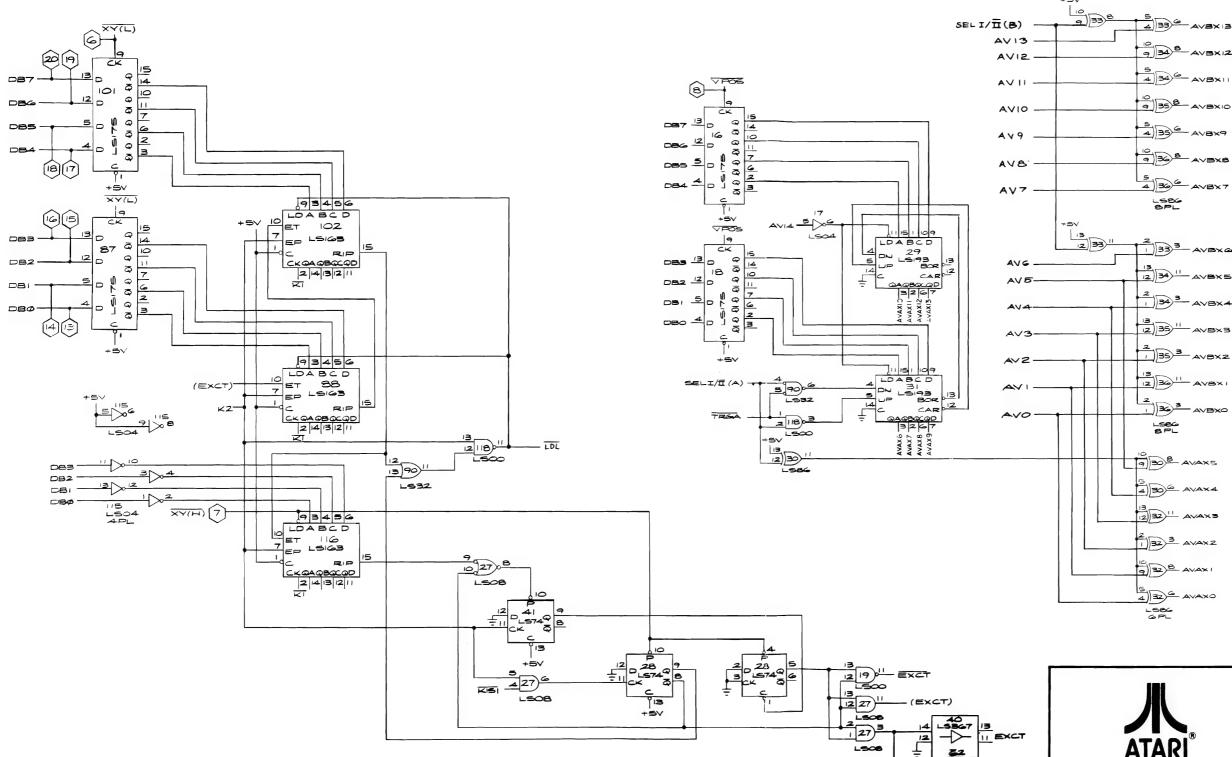
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SP-204 Sheet 8B

DMA Control

Dynamic RAM Video Address and Flip

[EXCT]





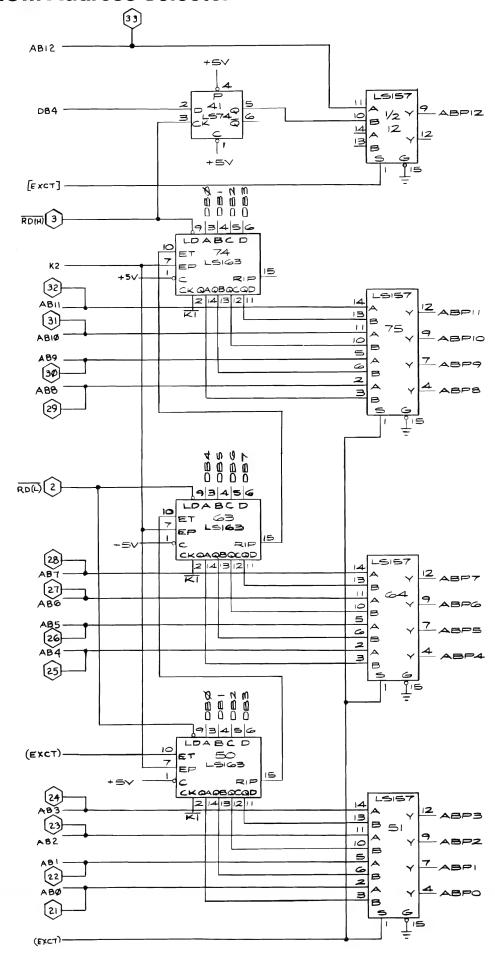
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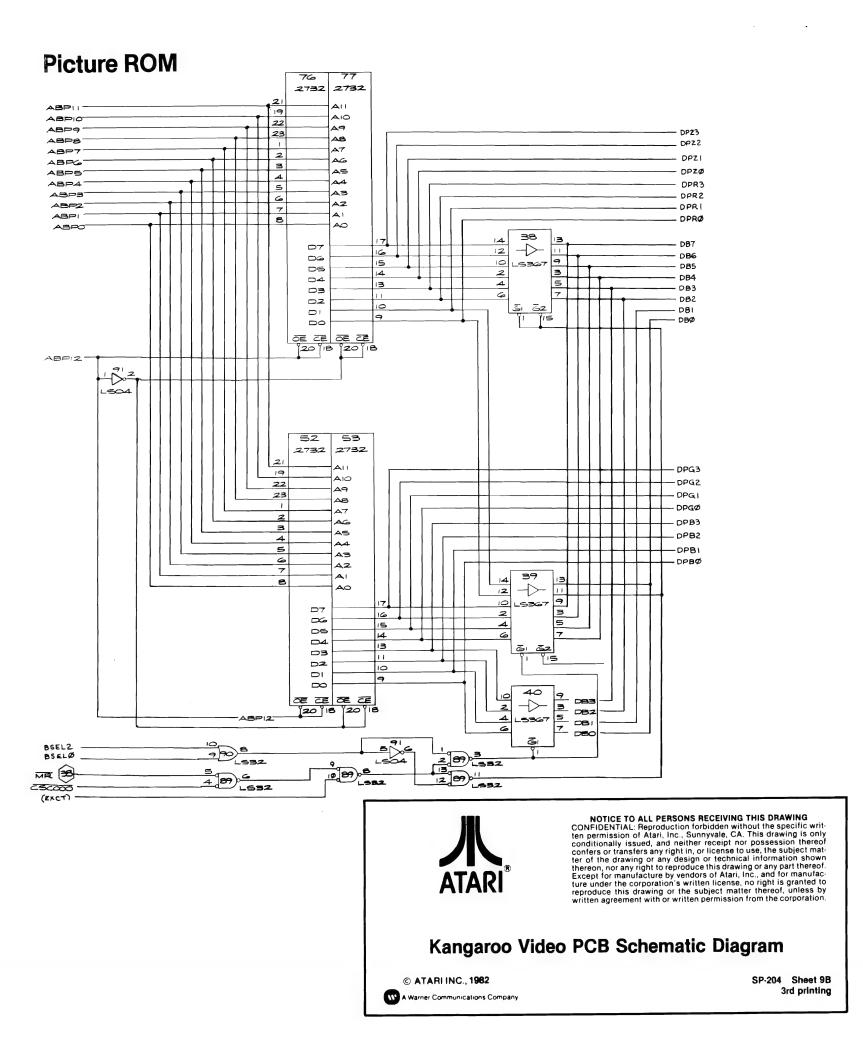
Kangaroo Video PCB Schematic Diagram

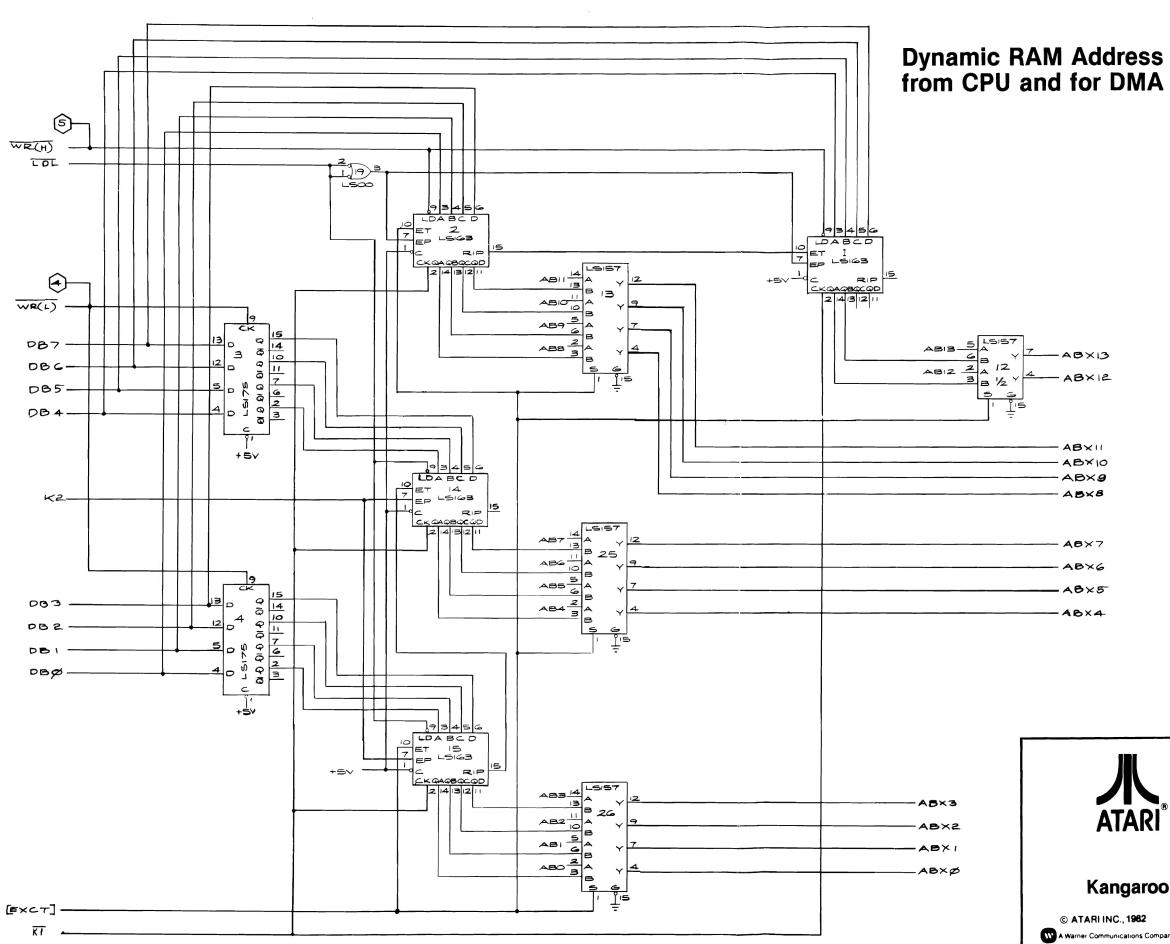
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Picture ROM Address Selector









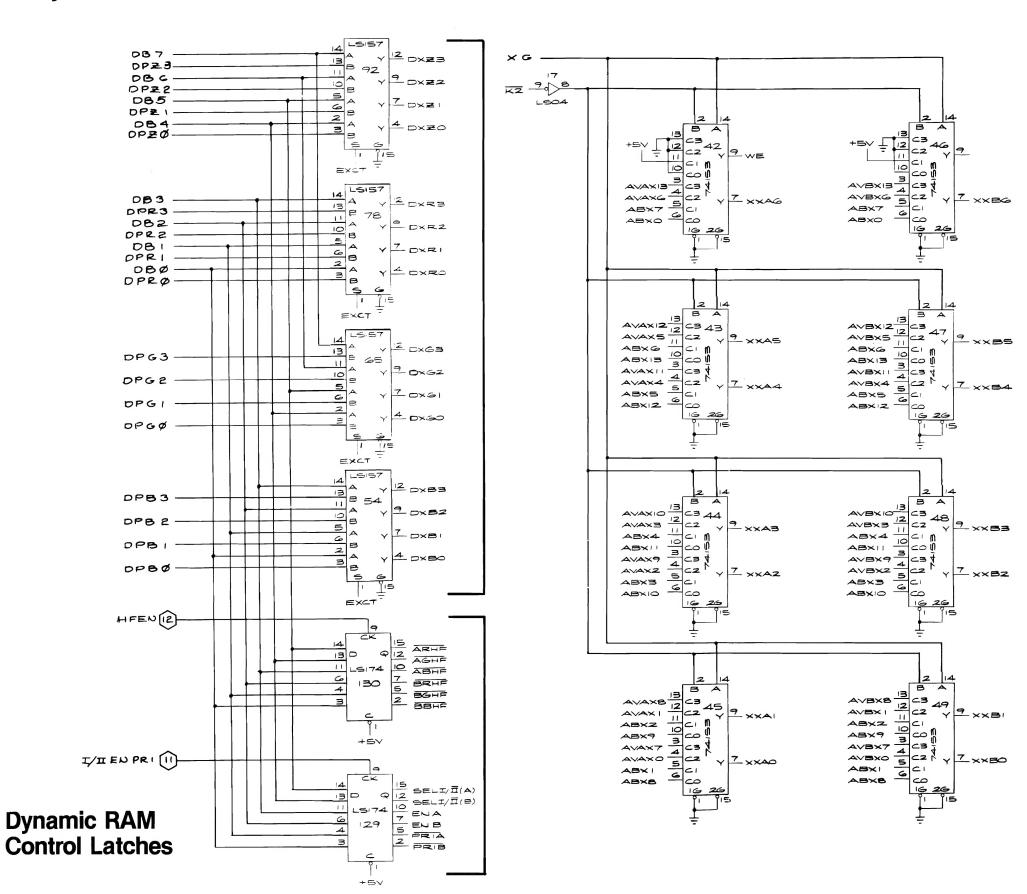
Kangaroo Video PCB Schematic Diagram

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Dynamic RAM Data Selector

Dynamic RAM Address Selector





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Kangaroo Video PCB Schematic Diagram

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